

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Mutsumi KIMURA

Application No.: 10/615,014

Filed: July 9, 2003

Docket No.: 116029

For:

THIN-FILM TRANSISTOR, SWITCHING CIRCUIT, ACTIVE ELEMENT SUBSTRATE, ELECTRO-OPTICAL DEVICE, ELECTRONIC APPARATUS, THERMAL HEAD, DROPLET EJECTING HEAD, PRINTER, AND THIN-FILM-TRANSISTOR DRIVING AND LIGHT-

EMITTING DISPLAY DEVICE

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- $\boxtimes$ This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- $\boxtimes$ Relevance of the non-English language references 1-12 is discussed in the present specification.

Respectfully submitted,

Registration No. 27,075

Joel S. Armstrong Registration No. 36,430

JAO:JSA/emt

Date: December 29, 2003

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ATTY DOCKET NO. 116029

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## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT Mutsumi KIMURA

FILING DATE July 9, 2003

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	1	Shimoda et al., "High Resolution Light Emitting Polymer Display Driven by Low Temperature Polysilicon Thin Film Transistor with Integrated Driver," Proceeding of Asia Display, pp. 217, 1998						
	2	Kimura et al., "Low-Temperature Polysilicon Thin-Film Transistor Driving with Integrated Driver for High-Resoluation Light Emitting Polymer Display," IEEE Transactions on Electron Devices, Vol. 46, No. 12, pp. 2282-2288, December 1999						
	3	Shimoda et al., "Current Status and Future of Light-Emitting Polymer Display Driven by Poly-Si TFT," SID 99 Digest, pp.372-375, 1999						
	4	Kimura et al., "TFT-LEPD with Image Uniformity by Area Ratio Gray Scale," Proceeding of Euro. Display, pp. 71-74,						
	5	Kimura et al., "Low-Temperature Poly-Si TFT Driven Light-Emitting-Polymer Displays and Digital Gray Scale for Uniformity," Proceeding of IDW, pp. 171-174, 1999						
	6	Tam et al., "Polysilicon TFT Drivers for Light Emitting Polymer Displays," IDW, pp. 175-178, 1999						
	7	Kimura et al., "An area-ratio gray-scale method to achieve image uniformity in TFT-LEPDs," Journal of the SID 8/2, pp. 93-97, 2000						
	8	Kimura et al., "Low-Temperature Poly-Si TFT Display using Light-Emitting-Polymer," AM-LCD, pp. 245-248, 2000						
	9	Tam et al., "Improved Polysilicon TFT Drivers for Light Emitting Polymer Displays," IDW, pp. 243-246, 2000						
	10	Inoue et al., "Investigation of the Relationship between Hot-Carrier Degradation and Kink Effect in Low-Temperature Poly-Si TFTs," SID Digest, pp. 452-455, 1999						
	11	Uraoka et al., "Hot Carrier Effects in Low-Temperature poly-Si p-ch TFTs under Dynamic Stress," AM-LCD, pp. 179-18 2001						
	12	Ohno et al., "Device Simulation of R 2000	eliability in Low Temp	perature Poly-Si TFTs,	" Technical Report of IEICE	, pp. 43-49,		
XAMINER	-				DATE CONSIDERED	<del></del>		

Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in

conformance and not considered. Include copy of this form with next communication to applicant.

Date: December 29, 2003

Examiner: